

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

William Ho Chang and
Vinaynathan Viswanathan

Date: August 24, 2010

Serial No. : 10/823,513

Group Art Unit 2191

Filed : April 12, 2004

For : AUTORUN FOR INTEGRATED CIRCUIT MEMORY COMPONENT

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

The U.S. Patent and Trademark Office issued a Notice of Allowance dated June 1, 2010 on the above-identified patent application. In the Notice, the Examiner allowed claims 1-6, 8-9 and 20-51 (renumbered as 1-40). The Examiner also paraphrased or restated portions of the claimed invention as part of the stated reasons for allowance. Applicants agree with the Examiner's conclusions regarding the patentability of the allowed claims, without necessarily agreeing with or acquiescing in the Examiner's reasoning. In particular, applicants believe that the application is allowable because the prior art fails to teach or suggest the invention as claimed, independent of how the invention is paraphrased or restated.

Please contact the undersigned regarding any questions or comments, or if a telephone call would expedite issuance of the patent.

CERTIFICATE OF ELECTRONIC TRANSMISSION

I hereby certify that this correspondence is being filed electronically via the EFS-Web system at www.uspto.gov on August 24, 2010.

/Heidi Dutro/
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Respectfully submitted,

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